**INFORMATION DISCLOSURE
CITATION FORM FOR
PATENT APPLICATION
(FORM PTO-1449)
(Substitute)**

Docket No.: 872.0178.U1(US)

Serial No.: 10/783,049

Applicant(s): Hooli, et al.

Filing Date: 2/19/04

Group: 2611

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
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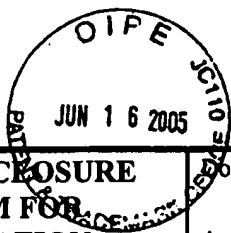
OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

/NM/	"Chip-Level Channel Equalization in WCDMA Downlink", Kari Hooli et al., EURASIP Journal on Applied Signal Processing 2002-8, pp. 757-770
/NM/	"A Generalized RAKE Receiver for Interference Suppression", Gregory E. Bottomley, et al., IEEE 2000, pp. 1536-1545
/NM/	"V-BLAST: An Architecture for Realizing Very High Data Rates Over the Rich-Scattering Wireless Channel", P.W. Wolniansky, et al., IEEE 1998, pp. 295-300.
/NM/	"Pilot-aided Adaptive Chip Equalizer Receiver for Interference Suppression in DS-CDMA Forward Link", Frederik Petre et al., IEEE 2000, pp. 303-308

Examiner's Signature: /Nurul Matin/

Date Considered: 03/07/2007

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
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Docket No.: 872.0178.U1(US)

Serial No.: 10/783,049

Applicant(s): Hooli et al.

Filing Date: February 19, 2004

Group: 2631 2611

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/NM/	PETRE, F. et al., "Pilot-aided Adaptive Chip Equalizer Receiver for Interference Suppression in DS-CDMA Forward Link", IEEE: VTC 2000, pages 303-308,

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/NM/	"Multi-antenna Transceiver Techniques for 3G and Beyond", Ari Hottinen et al., John Wiley & Sons, Chichester, UK, 2003, pp. 123-131

Examiner's Signature: /Nurul Matin/

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